

# Is Now Part of



# ON Semiconductor®

# To learn more about ON Semiconductor, please visit our website at www.onsemi.com

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at <a href="www.onsemi.com">www.onsemi.com</a>. Please email any questions regarding the system integration to Fairchild <a href="guestions@onsemi.com">guestions@onsemi.com</a>.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any EDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officer



June 2011

# NC7WZ14 TinyLogic<sup>®</sup> UHS Dual Inverter with Schmitt Trigger Inputs

#### **Features**

- Ultra-High Speed: t<sub>PD</sub> 3.2ns (Typical) into 50pF at 5V V<sub>CC</sub>
- High Output Drive: ±24mA at 3V V<sub>CC</sub>
- Broad V<sub>CC</sub> Operating Range: 1.65V to 5.5V
- Matches Performance of LCX when Operated at 3.3V V<sub>CC</sub>
- Power Down High Impedance Inputs/Outputs
- Over-Voltage Tolerance Inputs Facilitate 5V to 3V Translation
- Proprietary Noise/EMI Reduction Circuitry
- Ultra-Small MicroPak™ Packages
- Space-Saving SC70 Package

# Description

The NC7WZ14 is a dual inverter with Schmitt trigger input from Fairchild's Ultra-High Speed (UHS) Series of TinyLogic. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive while maintaining low static power dissipation over a very broad  $V_{\rm CC}$  operating range. The device is specified to operate over the 1.65V to 5.5V  $V_{\rm CC}$  range. The inputs and outputs are high-impedance when  $V_{\rm CC}$  is 0V. Inputs tolerate voltages up to 7V independent of  $V_{\rm CC}$  operating voltage. Schmitt trigger inputs achieve typically 1V hysteresis between the positive-and negative-going input threshold voltage at 5V.

# **Ordering Information**

Part Number	Operating Temperature	Top Mark	Package	Packing Method
NC7WZ14P6X	-40 to +85°C	Z14	6-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7WZ14EP6X	-40 to +125°C	Z14	6-Lead SC70, EIAJ SC-88a, 1.25mm Wide	3000 Units on Tape & Reel
NC7WZ14L6X	-40 to +85°C	A9	6-Lead MicroPak™, 1.00mm Wide	5000 Units on Tape & Reel
NC7WZ14FHX	-40 to +85°C	A9	6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch	5000 Units on Tape & Reel

# **Connection Diagrams**

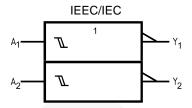


Figure 1. Logic Symbol

# **Pin Configurations**

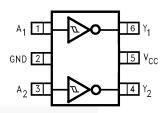


Figure 2. SC70 (Top View)

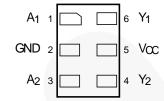
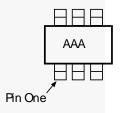


Figure 3. MicroPak (Top Through View)



#### Notes:

- 1. AAA represents Product Code Top Mark (see ordering code).
- 2. Orientation of Top Mark determines Pin One location. Read the top product code mark left to right. Pin One is the lower left pin.

Figure 4. SC70 Pin 1 Orientation

# **Pin Definitions**

Pin # SC70	Pin # MicroPak	Name	Description
1	1	A <sub>1</sub>	Input
2	2	GND	Ground
3	3	A <sub>2</sub>	Input
4	4	Y <sub>2</sub>	Output
5	5	V <sub>CC</sub>	Supply Voltage
6	6	Y <sub>1</sub>	Output

# **Function Table**

Y = /A

Inputs	Output
Α	Y
L	Н
Н	L

H = HIGH Logic Level

L = LOW Logic Level

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter			Min.	Max.	Unit
V <sub>CC</sub>	Supply Voltage			-0.5	7.0	V
V <sub>IN</sub>	DC Input Voltage			-0.5	7.0	V
V <sub>OUT</sub>	DC Output Voltage			-0.5	7.0	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < -0.5V			-50	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> < -0.5V			-50	mA
I <sub>OUT</sub>	DC Output Current			±100	mA	
I <sub>CC</sub> or I <sub>GND</sub>	DC V <sub>CC</sub> or Ground Current				±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65	+150	°C	
TJ	Junction Temperature Under Bia	IS			+150	°C
TL	Junction Lead Temperature (Sol	dering, 10 Seco	onds)		+260	°C
		SC70-6	T <sub>A</sub> =85°C		170	\ -
$P_{D}$	Power Discipation	3070-6	T <sub>A</sub> =125°C		104	mW
FD	Power Dissipation	MicroPak-6			130	IIIVV
			MicroPak2-6		120	
ESD	Human Body Model, JEDEC:JESD22-A114				4000	V
ESD	Charge Device Model, JEDEC:J	ESD22-C101			2000	V

# Recommended Operating Conditions<sup>(3)</sup>

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to Absolute Maximum Ratings.

Symbol	Parameter	Conditions	Min.	Max.	Unit	
V	Supply Voltage Operating		1.65	5.50	V	
$V_{CC}$	Supply Voltage Data Retention		1.5	5.5	V	
$V_{IN}$	Input Voltage		0	5.5	V	
V <sub>OUT</sub>	Output Voltage		0	V <sub>CC</sub>	V	
		SC70-6	-40	+125		
$T_A$	Operating Temperature	MicroPak-6	-40	+85	°C	
		MicroPak2-6	-40	+85		
		SC70-6		390		
$\theta_{JA}$	Thermal Resistance	MicroPak-6		500	°C/W	
		MicroPak2-6		560		

#### Note

3. Unused inputs must be held HIGH or LOW. They may not float.

# **DC Electrical Characteristics**

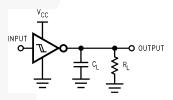
Symbol	Parameter	V <sub>cc</sub> (V)	Conditions	Т	<sub>A</sub> =+25°	°C		40 to 5°C		40 to 25°C	Units
•			,		Тур.	Max.	Min.	Max.	Min.	Max.	
		1.65		0.60		1.40	0.60	1.40	0.60	1.40	
		1.80		0.70		1.50	0.70	1.50	0.70	1.50	
\	Positive Threshold	2.30		1.00		1.80	1.00	1.80	1.00	1.80	,,
$V_P$	Voltage	3.00		1.30		2.20	1.30	2.20	1.30	2.20	V
		4.50		1.90		3.10	1.90	3.10	2.00	3.20	
		5.50		2.20		3.60	2.20	3.60	2.30	3.70	
		1.65		0.20	0.50	0.80	0.20	0.80	0.30	0.90	
		1.80		0.25	0.56	0.90	0.25	0.90	0.35	1.00	
\/	Negative Threshold	2.30		0.40	0.75	1.15	0.40	1.15	0.50	1.20	V
$V_N$	Voltage	3.00		0.60	0.98	1.50	0.60	1.50	0.70	1.60	V
		4.50		1.00	1.42	2.00	1.00	2.00	1.10	2.20	
		5.50		1.20	1.68	2.30	1.20	2.30	1.40	2.50	
- 7		1.65		0.10	0.48	0.90	0.10	0.90	0.10	0.90	
	9	1.80		0.15	0.51	1.00	0.15	1.00	0.15	1.00	
		2.30		0.25	0.62	1.10	0.25	1.10	0.25	1.10	1
$V_H$	Hysteresis Voltage	3.00		0.40	0.76	1.20	0.40	1.20	0.40	1.20	V
		4.50		0.60	1.01	1.50	0.60	1.50	0.60	1.50	
		5.50		0.70	1.20	1.70	0.70	1.70	0.70	1.70	
		1.65		1.55	1.65		1.55		1.55		
		1.80		1.70	1.80		1.70		1.70		
		2.30	V <sub>IN</sub> =V <sub>IL</sub> ,	2.20	2.30		2.20		2.20		
		3.00	I <sub>OH</sub> =-100μA	2.90	3.00		2.90		2.90		
	HIGH Level Output	4.50		4.40	4.50		4.40		4.40		V
$V_{OH}$	Voltage	1.65	I <sub>OH</sub> =-4mA	1.29	1.52		1.29		1.26		
		2.30	I <sub>OH</sub> =-8mA	1.90	2.14		1.90		1.80		
		3.00	I <sub>OH</sub> =-16mA	2.40	2.75		2.40		2.30		
		3.00	I <sub>OH</sub> =-24mA	2.30	2.62		2.30		2.20		
		4.50	I <sub>OH</sub> =-32mA	3.80	4.13		3.80		3.70		
$\rightarrow$	77.	1.65	GIT -		0.00	0.10		0.10		0.10	
		1.80	1		0.00	0.10		0.10		0.10	
	\.	2.30	V <sub>IN</sub> =V <sub>IH</sub> ,		0.00	0.10		0.10		0.10	
		3.00	I <sub>OL</sub> =100μΑ		0.00	0.10		0.10		0.10	
	LOW Lovel Output	4.50			0.00	0.10		0.10		0.10	
$V_{\text{OL}}$	LOW Level Output Voltage	1.65	I <sub>OL</sub> =4mA		0.08	0.24		0.24		0.26	V
		2.30	I <sub>OL</sub> =8mA		0.10	0.30		0.30		0.32	
	3.00 3.00	I <sub>OL</sub> =16mA		0.16	0.40		0.40		0.43		
			I <sub>OL</sub> =24mA		0.10	0.55		0.40		0.60	
		4.50	I <sub>OL</sub> =32mA		0.25	0.55		0.55		0.60	1
I <sub>IN</sub>	Input Leakage Current	0 to 5.5	V <sub>IN</sub> =5.5V, GND		3.20	±0.1		±1.0		±2.0	μA
I <sub>OFF</sub>	Power Off Leakage Current	0	V <sub>IN</sub> or V <sub>OUT</sub> =5.5V			1		10		20	μA
I <sub>cc</sub>	Quiescent Supply Current	1.65 to 5.50	V <sub>IN</sub> =5.5V, GND			1.0		10		20	μA

# **AC Electrical Characteristics**

Symbol	Parameter	V <sub>cc</sub> (V)	Conditions	T <sub>A</sub> =+25°C		T <sub>A</sub> =-40 to +85°C		T <sub>A</sub> =-40 to +125°C		Units	Figure	
				Min.	Тур.	Max.	Min.	Max.	Min.	Max.		
		1.65		2.5	7.6	13.1	2.5	14.5	2.5	14.7		
		1.80		2.5	6.3	10.9	2.5	12.0	2.5	12.3	ns Figure	Figure 5 Figure 6
		$2.50 \pm 0.20$	$C_L=15pF$ , $R_L=1M\Omega$	1.8	4.3	7.4	1.8	8.1	1.8	8.4		
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay	$3.30 \pm 0.30$		1.5	3.3	5.0	1.5	5.5	1.5	5.8		
		$5.00 \pm 0.50$		1.0	2.7	4.1	1.0	4.5	1.0	4.8		
		$3.30 \pm 0.30$	C <sub>L</sub> =50pF,	1.8	4.0	6.0	1.8	6.6	1.8	6.9		Figure 5
		$5.00 \pm 0.50$	R <sub>L</sub> =500Ω	1.2	3.2	4.9	1.2	5.4	1.2	5.7		Figure 6
C <sub>IN</sub>	Input Capacitance	0.00			2.5						pF	
	Power Dissipation	3.30			11.0						nE.	Figure 7
OPD	Capacitance <sup>(4)</sup>	5.00			12.5						pF	Figure 7

#### Note:

4. C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I<sub>CCD</sub>) at no output loading and operating at 50% duty cycle. C<sub>PD</sub> is related to I<sub>CCD</sub> dynamic operating current by the expression: I<sub>CCD</sub>=(C<sub>PD</sub>)(V<sub>CC</sub>)(f<sub>IN</sub>)+(I<sub>CC</sub>static).



#### Note:

5. CL includes load and stray capacitance; Input PRR=1.0MHz;  $t_W$ =500ns

Figure 5. AC Test Circuit

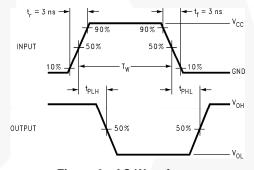
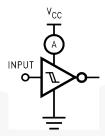


Figure 6. AC Waveforms



#### Note:

6. Input=AC Waveform; t<sub>r</sub>=t<sub>f</sub>=1.8ns; PRR=variable; Duty Cycle =50%. Figure 7. I<sub>CCD</sub> Test Circuit

# **Physical Dimensions**

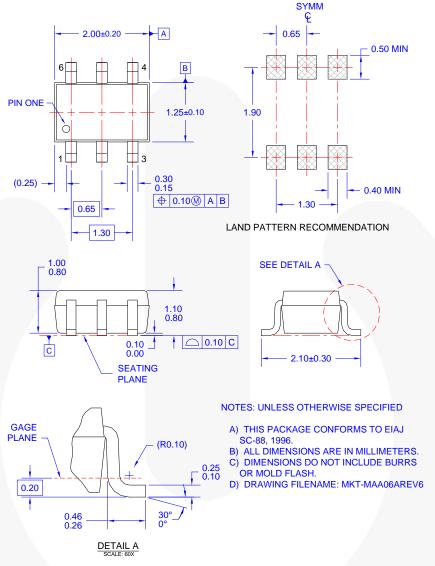


Figure 8. 6-Lead, SC70, EIAJ SC-88a, 1.25mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

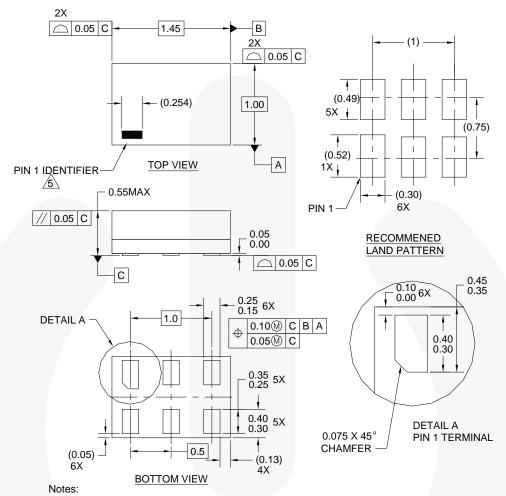
Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

# **Tape and Reel Specification**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: <a href="http://www.fairchildsemi.com/products/analog/pdf/sc70-6\_tr.pdf">http://www.fairchildsemi.com/products/analog/pdf/sc70-6\_tr.pdf</a>.

Package Designator	Tape Section	Tape Section Cavity Number		Cover Type Status
	Leader (Start End)	125 (Typical)	Empty	Sealed
P6X	Carrier	3000	Filled	Sealed
	Trailer (Hub End)	75 (Typical)	Empty	Sealed

# **Physical Dimensions**



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994
- 4. FILENAME AND REVISION: MAC06AREV4
- DIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

Figure 9. 6-Lead, MicroPak™, 1.0mm Wide

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

# **Tape and Reel Specification**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/products/logic/pdf/micropak\_tr.pdf.

Package Designator	Tape Section	Cavity Number	Cavity Status	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
L6X	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	

# **Physical Dimensions**

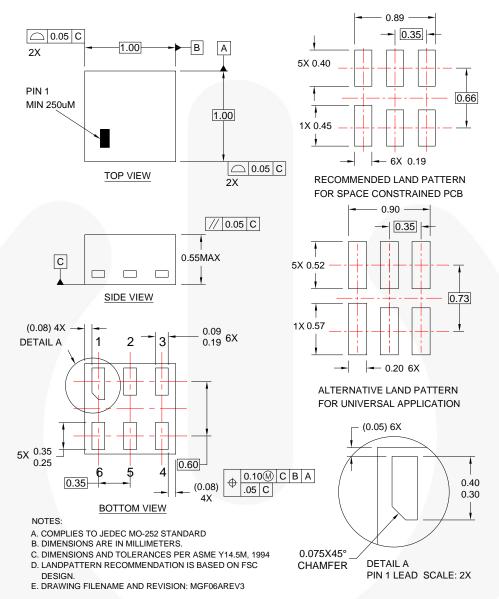


Figure 10. 6-Lead, MicroPak2, 1x1mm Body, .35mm Pitch

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.

Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings: http://www.fairchildsemi.com/packaging/.

# **Tape and Reel Specification**

Please visit Fairchild Semiconductor's online packaging area for the most recent tape and reel specifications: http://www.fairchildsemi.com/packaging/MicroPAK2 6L tr.pdf.

Package Designator	Tape Section	<b>Cavity Number</b>	<b>Cavity Status</b>	Cover Type Status	
	Leader (Start End)	125 (Typical)	Empty	Sealed	
FHX	Carrier	5000	Filled	Sealed	
	Trailer (Hub End)	75 (Typical)	Empty	Sealed	





#### TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

2Cool™ AccuPower™ Auto-SPM™ AX-CAP™\* BitSiC®

Global Power Resource<sup>SM</sup> Green FPS™ Build it Now™ Green FPS™ e-Series™ CorePLUS™ Gmax™ CorePOWER\*\* CROSSVOLT\*\*

CTLTM. Current Transfer Logic™ DEUXPEED<sup>6</sup> Dual Cool™ EcoSPARK® EfficientMax™ ESBC™

Fairchild® Fairchild Semiconductor® FACT Quiet Series™ FACT FAST® FastvCore™ FETBench™

FlashVVriter®\* **FPSTM** F-PEST FREET

GTO™ IntelliMAX\*\* ISOPLANAR™ MegaBuck™ MICROCOUPLER™ MicroFET\*\* MicroPak™ MicroPak2™

MotionMax™ Motion-SPM™ mWSaver™ OptoHiT™ OPTOLOGIC® OPTOPLANAR®

MillerDrive™

PDP SPM™ Power-SPM™ PowerTrench® PowerXS™

Programmable Active Droop™ OFFT

OSTM Quiet Series™ RapidConfigure™

Saving our world, 1mW/W/kW at a time™ SignalWise™

SmartMax™ SMART START™ SPM® STEALTH™ SuperFET<sup>6</sup> SuperSOT™3 SuperSOTM-6 SuperSOT™-8 SupreMOS® SyncFET™ Sync-Lock™

SYSTEM ..

TinyBoost™ TinyBuck™ TinyCalc™ TinyLogic<sup>®</sup> TINYOPTO\*\* TinyPower™ TinyPVM™ TinyWire™ TranSiC<sup>6</sup> TriFault Detect™ TRUECURRENT®\* μSerDes™ Ultra FRFET™ UniFFT\*\* **VCXTM** VisualMax™

XSTM

p wer

The Power Franchise®

The Right Technology for Your Success™

\* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN, NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THERBIN, WHICH COVERS THESE PRODUCTS

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user
- 2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Sales Support.

Counterfeiting of serriconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors

#### PRODUCT STATUS DEFINITIONS

# Definition of Torms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 155

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdt/Patent-Marking.pdf">www.onsemi.com/site/pdt/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and exp

# **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative